

JHK100N120FA

Product Preview

1200V/100A FIELD-STOP TRENCH IGBT WITH DIODE



Features

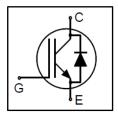
- Low V_{CE(sat)}
- Fast Switching
- High Ruggedness
- Short-Circuit Rated



Product Summary				
V _{CES}	1200V			
lc	100A ⁽¹⁾			
V _{CE(sat),typ}	1.65V (T _J = 25°C)			
Package	TO-264			

Applications

- Inverters
- Frequency Converters
- Industrial Motor Drives
- Uninterrupted Power Supply





Ordering Information

Part Number	Marking	Package	Packing
JHK100N120FA	HK100N120FA	TO-264	Tube

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit
Collector-to-Emitter Voltage	V _{CES}	1200	V
Gate-to-Emitter Voltage	V _{GES}	±20	V
DC Collector Current (T _c = 25°C, T _J = 150°C)		160 ⁽²⁾	
DC Collector Current (T _c = 100°C, T _J = 150°C)	- I _C	103	
Pulsed Collector Current (pulse width limited by maximum T _J)	I _{CM}	300] ,
Diode Forward Current (T _c = 25°C, T _J = 150°C)		160 (2)	A
Diode Forward Current (T _c = 100°C, T _J = 150°C)	l _F	100	
Diode Pulsed Current (pulse width limited by maximum T _J)	I _{FM}	300	
Short Circuit Withstand Time ($V_{GE} = 15V$, $V_{CC} \le 600V$, $T_{J_start} \le 150$ °C)	t _{SC}	10	μs
Turn-off Safe Operating Area (V _{CE} ≤ 1200V, T _J ≤ 150°C)	-	300	Α
Maximum Power Dissipation (T _c = 25°C, T _J = 150°C)	P _{D(max)}	696	W
Operating Junction Temperature	TJ	-40 to +150	
Storage Temperature	T _{stg}	-55 to +150	°C
Maximum Lead Temperature for Soldering (1/8" from case for 5 seconds)	T _{sld}	260	



Static Electrical Characteristics (3)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Collector-to-Emitter Breakdown Voltage	BV _{CES}	$V_{GE} = 0V$, $I_C = 250\mu A$	1200	-	ı	V
		$V_{CE} = 1200V, V_{GE} = 0V$	-	-	10	μΑ
Collector-to-Emitter Leakage Current	I _{CES}	$V_{CE} = 1200V$, $V_{GE} = 0V$,			5	mΛ
		T _J =150°C	-	-	5	mA
Gate-to-Emitter Leakage Current	I _{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	100	nA
Gate Threshold Voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 1.5$ mA	5.5	6.5	7.5	
		V _{GE} = 15V, I _C = 100A	-	1.65	2.0	
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	V _{GE} = 15V, I _C = 100A,	_	2.2	-	
		T _J =150°C				V
		$V_{GE} = 0V$, $I_F = 100A$	-	1.85	2.25	
Diode Forward Voltage	V _F	$V_{GE} = 0V$, $I_F = 100A$	_	1.6	-	
		T _J =150°C		2.0		

Thermal Characteristics

Parameter	Symbol	Min	Тур	Max	Unit
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$	-	-	25	
Junction-to-Case Thermal Resistance, IGBT	D	-	-	0.18	°C/W
Junction-to-Case Thermal Resistance, Diode	R _{θJC}	-	-	0.26	

Dynamic Electrical Characteristics (3)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Total Gate Charge	Qg	$V_{CC} = 600V,$ $V_{GE} = 15V,$ $I_{C} = 100A$	-	478	-	nC
Input Capacitance	C _{iss}	V _{CE} = 25V,	-	9605	-	
Output Capacitance	C _{oss}	$V_{GE} = 0V$,	-	413	-	pF
Reverse Transfer Capacitance	C _{rss}	f = 1MHz	-	98	-	



Switching Characteristics, Inductive Load (3), (4)

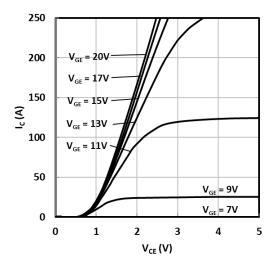
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Turn-on Delay time	t _{d(ON)}		-	62	-	
Rise Time	t _r	$V_{CC} = 600V,$ $V_{GE} = 0/15V,$	-	90	-	
Turn-off Delay time	t _{d(OFF)}	$R_G = 1\Omega$, $I_C = 100A$,	-	280	-	ns
Fall Time	t _f	$L_{load} = 0.82 \text{mH},$	-	119	-	
Turn-On Switching Loss	E _{on}	Energy losses include "tail" and diode reverse recovery.	-	9.37	-	
Turn-Off Switching Loss	E _{off}		-	5.07	-	mJ
IGBT Total Switching Loss	E _{ts}		-	14.44	-	
Diode Reverse-Recovery Time	t _{rr}	V _R = 600V,	-	249	-	ns
Diode Reverse-Recovery Charge	Q _{rr}	I _F = 100A,	-	5160	-	nC
Diode Peak Reverse-Recovery Current	I _{rrm}	dI _F /dt = 900A/μs	-	37	-	Α
Short Circuit Collector Current	I _{C(SC)}	V_{GE} = 15V, $V_{CC} \le 600V$, $t_{SC} \le 10\mu s$	-	350	-	А

- (1) DC collector current, $T_c = 102$ °C, $T_J = 150$ °C.
- (2) Limited by bonding wire
- (3) $T_J = 25$ °C unless otherwise specified
- (4) $t_{r}\!\!:$ from 10% of Ic to 90% of Ic; $t_{f}\!\!:$ from 90% of Ic to 10% of Ic;

 $E_{on}\!:$ from 10% of V_{GE} to 10% of $V_{CE};\quad E_{off}\!:$ from 90% of V_{GE} to 10% of Ic.



Typical Electrical Characteristics





$$(T_J = 25 \, ^{\circ}C, t_p = 250 \, \mu s)$$

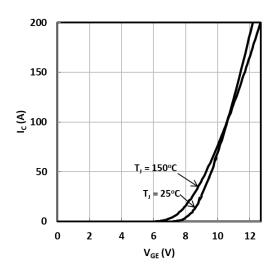


Fig. 3 Typical transfer characteristics

$$(V_{CE} = 20 \text{ V}, t_p = 250 \mu\text{s})$$

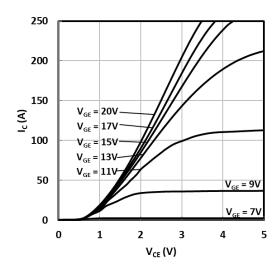


Fig. 2 Typical output characteristics

$$(T_J = 150 \, ^{\circ}\text{C}, \, t_p = 250 \, \mu\text{s})$$

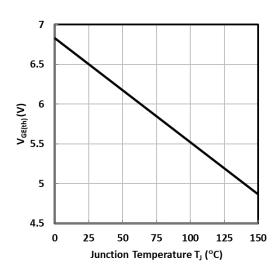


Fig. 4 Typical gate threshold voltage as a function of junction temperature

$$(V_{CE} = V_{GE}, I_C = 1.5mA)$$



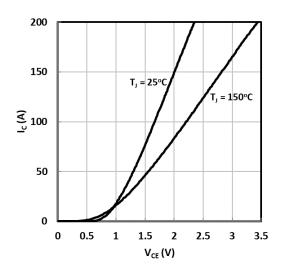


Fig. 5 Typical saturation voltage characteristics $(V_{GE}=15\ V,\,t_p=250\ \mu s)$

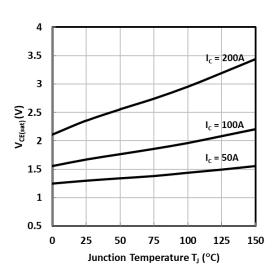


Fig. 6 Typical saturation voltage as a function of junction temperature

$$(V_{GE} = 15 \text{ V}, t_p = 250 \mu s)$$

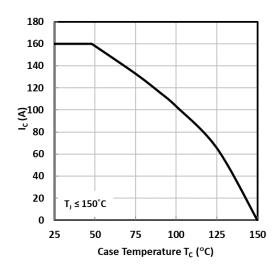


Fig. 7 Maximum DC collector current as a function of case temperature

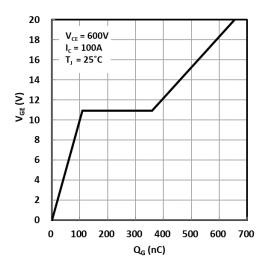


Fig. 8 Typical gate charge characteristics



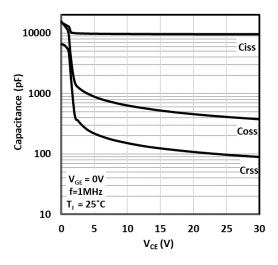


Fig. 9 Typical capacitance as a function of collector-to-emitter voltage

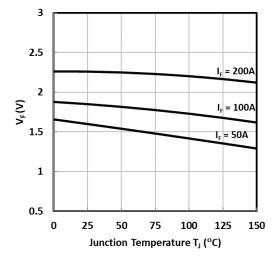


Fig. 11 Typical diode forward voltage as a function of junction temperature

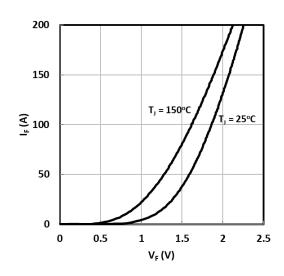
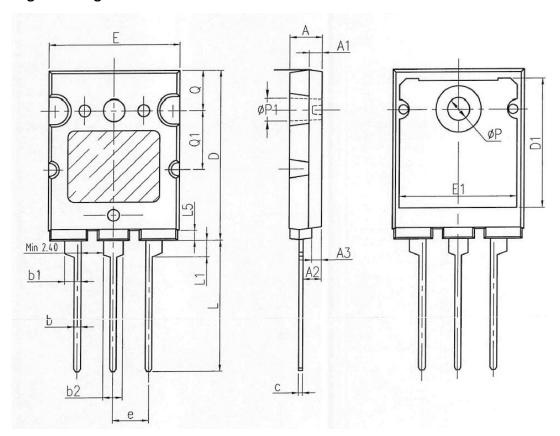


Fig. 10 Typical diode forward current as a function of forward voltage

$$(V_{GE} = 0 V, t_p = 250 \mu s)$$



Package Drawing



		mm	
SYMBOL	MIN	NOM	MAX
Α	4. 80	5. 00	5. 20
A1	2	2.00	REF
A2	2.50	2.80	3. 10
A3		1.50	REF
b	0.90	1.00	1. 25
b1	2. 30	2. 50	2. 75
b2	2. 80	3.00	3. 20
С	0.50	0.60	0.85
D	25. 70	26.00	26. 30
D1	19.00	_	_
Е	19.50	20.00	20.50
E1	16.00	_	-
е		5. 45	TYP
L	19.50	20.00	20.50
L1	2. 20	2. 50	2.70
L5		1.35	REF
ΦР	3. 00	3. 20	3. 40
ФР1	3. 20	3. 40	3. 60
Q	5. 80	6.00	6. 20
Q1	8.80	9. 00	9. 20

TO-264





Revision history of JHK100N120FA Specification

Version	Change Items	Effective Date	
1.00	Initial Release	10-Aug-21	



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